

L Number	Hits	Search Text	DB	Time stamp
1	0	((timing adj system) and control\$4 and (data adj transfer) and ((embedded adj dram) or (embedded near3 memory)) and bias and receiv\$3	USPAT; US-PGPUB; EPO; JPO	2003/04/18 17:41
2	58	((timing adj system) and control\$4 and (data adj transfer) and bias and receiv\$3	USPAT; US-PGPUB; EPO; JPO	2003/04/18 17:42
3	0	((timing adj system) and control\$4 and (data adj transfer) and bias and receiv\$3) and dram and embed\$3	USPAT; US-PGPUB; EPO; JPO	2003/04/18 17:43
4	13	((timing adj system) and control\$4 and (data adj transfer) and bias and receiv\$3) and dram	USPAT; US-PGPUB; EPO; JPO	2003/04/18 17:43

L Number	Hits	Search Text	DB	Time stamp
1	0	((timing adj system) and control\$4 and (data adj transfer) and ((embedded adj dram) or (embedded near3 memory)) and bias and receiv\$3	USPAT; US-PGPUB; EPO; JPO	2003/04/18 17:41
2	58	((timing adj system) and control\$4 and (data adj transfer) and bias and receiv\$3	USPAT; US-PGPUB; EPO; JPO	2003/04/18 17:43
3	0	((timing adj system) and control\$4 and (data adj transfer) and bias and receiv\$3) and dram and embed\$3	USPAT; US-PGPUB; EPO; JPO	2003/04/18 17:43
4	13	((timing adj system) and control\$4 and (data adj transfer) and bias and receiv\$3) and dram	USPAT; US-PGPUB; EPO; JPO	2003/04/18 18:02
5	47	((timing adj system) and control\$4 and (data adj transfer) and bias and receiv\$3) and clock	USPAT; US-PGPUB; EPO; JPO	2003/04/18 18:02
6	47	((timing adj system) and control\$4 and (data adj transfer) and bias and receiv\$3) and clock) and output	USPAT; US-PGPUB; EPO; JPO	2003/04/18 18:03
7	13	((timing adj system) and control\$4 and (data adj transfer) and bias and receiv\$3) and dram) and (output adj clock)	USPAT; US-PGPUB; EPO; JPO	2003/04/18 18:03

EAST - [default1.wsp:1]

File View Edit Tools Window Help

☐ Drafts  
☐ Pending  
☒ Active  
     ☒ L1: (995) (edram or (embed  
     ☒ L2: (29518) (timing adj sy  
     ☒ L3: (16) 1 and 2  
☐ Failed  
☐ Saved  
☐ Favorites  
☐ Tagged (3)  
☐ UDC  
☐ Queue

Search List Browse Queue Clear  
 DBs: USPAT;US-PGPUB;EPO;JPO ☒ Plurals  
 Default operator: OR ☒ Highlight all hit terms initial

BRS f... IS&R... Image Text HTML

	Type	L #	Hits	Search Text	DBs	Time
1	BRS	L1	995	(edram or (embedded adj dram))	USPAT	2003/04/18
2	BRS	L2	29518	(timing adj system) or (timing adj circuit)	USPAT	2003/04/18
3	BRS	L3	16	1 and 2	USPAT	2003/04/18

Start [Icons] C:\Do... Explor... Explor... Inbox... Imp... (203)... EAS... EAST... 6:39 PM

EAST - [default1.wsp:1]

File View Edit Tools Window Help

Active

- ☒ L1: (0) (timing adj system
- ☒ L2: (58) (timing adj system
- ☒ L3: (0) 2 and dram and emb
- ☒ L4: (13) 2 and dram
- ☒ L5: (47) 2 and clock
- ☒ L6: (47) 5 and output
- ☒ L7: (13) 4 and (output adj

Search List Browse Queue Clear

DBs: USPAT:US-PGPUB:EPO:JPO ☒ Plurals

Default operator: OR ☒ Highlight all hit terms initial

BRS f... IS&R... Image Text HTML

	Type	L #	Hits	Search Text	DBs	Time
1	BRS	L1	0	(timing adj system) and control\$4 and (data adj transfer) and ((embedded adj dram) or (embedded near3 memory)) and bias and receiv\$3	USPAT ; US-PG	2003/04/18
2	BRS	L2	58	(timing adj system) and control\$4 and (data adj transfer) and bias and receiv\$3	USPAT ; US-PG	2003/04/18
3	BRS	L3	0	2 and dram and embed\$3	USPAT ; US-PG	2003/04/18
4	BRS	L4	13	2 and dram	USPAT ; US-PG	2003/04/18
5	BRS	L5	47	2 and clock	USPAT ; US-PG	2003/04/18
6	BRS	L6	47	5 and output	USPAT ; US-PG	2003/04/18
7	BRS	L7	13	4 and (output adj clock)	USPAT ; US-PG	2003/04/18

Start

6:11 PM

EAST - [default1.wsp:1]

File View Edit Tools Window Help

☐ Drafts  
☐ Pending  
☒ Active  
     L1: (0) (timing adj system  
     L2: (58) (timing adj syste  
     L3: (0) 2 and dram and emb  
     L4: (13) 2 and dram  
☐ Failed  
☐ Saved  
☐ Favorites  
☐ Tagged (0)  
☐ UDC

Search List Browse Queue Clear  
 DBs: USPAT; US-PGPUB; EPO; JPO ☒ Plurals  
 Default operator: OR ☒ Highlight all hit terms initial

BRS 1... IS&R... Image Text HTML

	Type	L #	Hits	Search Text	DBs	Time
1	BRS	L1	0	(timing adj system) and control\$4 and (data adj transfer) and ((embedded adj dram) or (embedded near3 memory)) and bias and receiv\$3	USPAT	2003/04/18
2	BRS	L2	58	(timing adj system) and control\$4 and (data adj transfer) and bias and receiv\$3	USPAT	2003/04/18
3	BRS	L3	0	2 and dram and embed\$3	USPAT	2003/04/18
4	BRS	L4	13	2 and dram	USPAT	2003/04/18

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